

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.35478CX23	SERIAL NO. 10/810,624
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)		APPLICANT Miwa, et al.	
		FILING DATE March 29, 2004	GROUP <del>2818</del> 2827

## U.S. PATENT DOCUMENTS

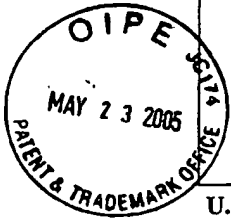
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
AB						
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## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Abstract	
						Yes	No
MS	AM 3-237692	10-23-91	JP			X	
MS	AN 11-025682	1-29-99	JP			X	
MS	AO 07-093979	04-07-95	JP			X	
	AP						
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	AR						
	AS						
	AT						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AU	
	AV	
	AW	
	AX	
	AY	
	AZ	
Examiner	M. TRAN	Date Considered 9/2/05



SHEET 1 OF 5

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Commerce (Rev. 4/92) Patent and Trademark  
Office

ATTY. DOCKET NO.

SERIAL NO.

501.35478CX23

Not yet assigned

10/810624

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

APPLICANT  
MIWA, et al

FILING DATE  
March 29, 2004

GROUP

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
MS	4 9 6 4 0 7 9	10/90	Devin			
I	5 3 6 5 4 8 6	11/94	Schreck			
I	5 4 1 2 6 0 1	5/95	Sawada et al	365	185.03	
I	5 8 3 1 9 0 0	11/98	Miyamoto	365	185.03	
MS	5 7 4 5 4 0 9	4/98	Wong et al	365	185.03	
I	5 5 5 5 2 0 4	9/96	Endoh et al	365	189.01	
I	5 1 5 1 3 8 7	9/92	Brady et al	437	191	4/30/90
I	5 0 8 4 4 1 7	1/92	Joshi et al	437	192	3/13/89
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						YES	NO
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MS	Digest of Technical Papers, ATP 2.1: A 3.3V 128Mb Multi-level NAND Flash Memory for Mass Storage Applications, T. Jung, et al., ISSCC96/Session 2/Flash Memory/Paper TP 2.1, 1996 EEE International Solid State Circuits Conf.
MS	Betty Prince, "Semiconductor Memories", 1983, Wisely, 2 <sup>nd</sup> Edition, pp. 620-621.

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EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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